

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S96	1	((((integrated adj circuit) or IC) with output\$1 with protect\$3) same (register\$1 or flip-flop\$1) same ((TAP or (test adj access adj port)) with controller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/06 10:42
S97	1	(((((integrated adj circuit) or IC) with output\$1 with protect\$3) same (register\$1 or flip-flop\$1))and (((TAP or (test adj access adj port)) with controller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/06 10:43
S98	9	((((integrated adj circuit) or IC) with output\$1 with protect\$3) and (register\$1 or flip-flop\$1) and ((TAP or (test adj access adj port)) with controller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/06 10:46
S99	1	((((integrated adj circuit) or IC) with output\$1 with protect\$3) and (register\$1 or flip-flop\$1) and ((TAP or (test adj access adj port)) with controller) and (disabl\$3 with output with short)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/06 10:47
S100	1	((((integrated adj circuit) or IC) with output\$1 with protect\$3) and ((TAP or (test adj access adj port)) with controller) and (disabl\$3 with output with short)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/06 10:48
S101	23	((((integrated adj circuit) or IC) with output\$1 with protect\$3) and (disabl\$3 with output with short)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/06 10:48
S102	12	((((integrated adj circuit) or IC) with output\$1 with protect\$3) same (disabl\$3 with output with short)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/06 19:18
S103	2	(((((integrated adj circuit) or IC) with output\$1 with protect\$3) same (disabl\$3 with output with short) same (clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/06 10:49

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S10 9	4	(714/726.ccls.) and ((disabl\$3 with (output adj buffer) with (short\$3 or overload\$3)) and ((integrated adj circuit) or IC))	US-PGPUB; USPAT; USOCR	OR	OFF	2007/07/08 17:35
S11 0	4	(714/727.ccls.) and ((disabl\$3 with (output adj buffer) with (short\$3 or overload\$3)) and ((integrated adj circuit) or IC))	US-PGPUB; USPAT; USOCR	OR	OFF	2007/07/08 17:36
S11 1	0	(714/724.ccls.) and ((disabl\$3 with (output adj buffer) with (short\$3 or overload\$3)) and ((integrated adj circuit) or IC))	US-PGPUB; USPAT; USOCR	OR	OFF	2007/07/08 17:36
S11 2	1	(326/29.ccls.) and ((disabl\$3 with (output adj buffer) with (short\$3 or overload\$3)) and ((integrated adj circuit) or IC))	US-PGPUB; USPAT; USOCR	OR	OFF	2007/07/08 17:37
S11 3	14	((disabl\$3 with (output adj buffer) with (short\$3 or overload\$3)) and ((integrated adj circuit) or IC))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/08 17:38
S11 4	9	((disabl\$3 with (output adj buffer) with (short\$3 or overload\$3)) and ((integrated adj circuit) or IC)) and (boundary adj scan adj register\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/08 17:43
S11 6	9	weinraub-chananiel.IN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/07/08 17:44

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John Colter, Netscape Navigator

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- Exclude pages by using a - if a search term must not appear on a page.

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Sun, 8 Jul 2007, 10:07:46 PM EST

Search Query Display

Recent Search Queries

#1

(disable an output buffer<and>short circuit)<and>integrated circuit

0

#2

((disabling an output buffer<and>short circuit)<and>integrated circuit<IN>metadata)

0

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